



Specifications

Sample Rate

Internal Clock (Timing Mode) 100Hz~200MHz

External Clock (State Mode) 100MHz

Threshold Voltages

Bandwidth : 75MHz
 Working Range : -6V~+6V
 Accuracy : ±0.1V

Memory

Memory : 4Mbits
 Depth(Per Channel) : 128Kbits (Max 32Mbits for compression)

Trigger

Condition : Pattern/Edge
 Trigger Channel : 16 CH
 Post Trigger : YES
 Trigger Level : 1Level
 Trigger Count : 1~65535

Software Functions

Data Compression : Max 128K bits x 256
 Time Base Range : 5ps~10Ms
 Language : Chinese (Traditional/Simplified)
 English

Maximum Trigger Page : 8192 Pages

Waveform Data Display

Filter&Filter Delay

Trigger Delay

Unlimited Increasing Bar

Automatic Attaching Bar

Automatic Software Upgrade

Data Statistic

Auto-Save

Filter Bar

Protocol Analysis

Protocol Packet List

File Export

Data Contrast : Not support

Latch Function : Not support

Protocol Analyzer Trigger : Parallel

Pulse Width Trigger Module: option

Other

Power : USB (DC 5V, 500mA)

Operating System : Windows 2000 / XP(32bits) / Vista / Win 7

Phase Errors : < 1.5ns

Maximum Input Voltage : ±30V

Impedance : 500KΩ/10pF

Safety Certification : FCC / CE / WEEE / RoHS / reach

Extending Channel Capture

Not support

Double Mode

Channels : 16 channels

RAM Sizes : 256K

Electric Specification

Item	Min	Typical	Max
Working Voltage	DC 4.5V	DC 5V	DC 5.5V
Current at Rest	DC 4.5V	DC 5V	200mA
Current at Work	DC 4.5V	DC 5V	400mA
Power at Rest	DC 4.5V	DC 5V	1W
Power at Work	DC 4.5V	DC 5V	2W
Vinput of Testing Channels	- DC30V	DC 5V	+DC30V
Vreference	- DC6V	DC 5V	+ DC6V
Impedance	- DC6V	500KΩ/10pF	+ DC6V
Working Temperature	5°C	500KΩ/10pF	70°C
Storage Temperature	-40°C	500KΩ/10pF	80°C

Marketed by-

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